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PATENT
(5500-68500/TT4418)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/061, 671

Filed: February 1, 2002

Inventor(s):

Michael A. Filippo

Examiner: Wang, Albert C.

Group/Art Unit: 2115

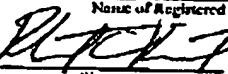
Atty. Dkt. No: 5500-68500

Title: Apparatus and Method for
Selective Clock Control to
Reduce Power Consumption
in an Integrated Circuit

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Robert C. Kowert
Name of Registered Representative



July 29, 2005
Date

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PAGE 12/13*RCVD AT 7/29/2005 2:13:59 PM (Eastern Daylight Time)*SVR:USPTO-EFXRF-6/25*DNIS:2738300*CSID: *DURATION (mm:ss):02:42

4. In making the above disclaimer, Advanced Micro Devices, Inc. does not disclaim the terminal part of any patent granted on the captioned patent application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. §154 to §156 and §173 of co-pending Application No. 10/061,792, as presently shortened by any terminal disclaimer, in the event that it later expires for failure to pay a maintenance fee, is held unenforceable, is found invalid by a court of competent jurisdiction, is statutorily disclaimed in whole or terminally disclaimed under 37 C.F.R. §1.321, has all claims canceled by a reexamination certificate, is reissued, or is in any manner terminated prior to the expiration of its full statutory term as presently shortened by any terminal disclaimer.

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Respectfully submitted,


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Date: July 29, 2005